

DERWENT-ACC-NO: 1990-004904

DERWENT-WEEK: 199001

COPYRIGHT 1999 DERWENT INFORMATION LTD

TITLE: Semiconductor IC device - has
connection of logic circuits to clock generator at centre
of chip NoAbstract Dwg 1/2

INVENTOR: YAMAGUCHI, S

PATENT-ASSIGNEE: MATSUSHITA ELEC IND CO LTD [MATU]

PRIORITY-DATA: 1988JP-0118521 (May 16, 1988)

PATENT-FAMILY:

PUB-NO	PAGES	PUB-DATE	
LANGUAGE		MAIN-IPC	
JP 01289155 A		November 21, 1989	N/A
000	N/A		
US 5239215 A		August 24, 1993	N/A
010	H03K 019/003		

APPLICATION-DATA:

PUB-NO	APPL-DESCRIPTOR	APPL-NO
APPL-DATE		
JP 01289155A	N/A	
1988JP-0118521	May 16, 1988	
US 5239215A	Div ex	
1989US-0351221	May 15, 1989	
US 5239215A	N/A	
1992US-0980413	November 20, 1992	

INT-CL (IPC): H01L027/04, H03K019/003

ABSTRACTED-PUB-NO: US 5239215A

EQUIVALENT-ABSTRACTS:

The semiconductor IC circuit chip includes several clock signals each having different phase, and multiple logic circuit sections coupled to receive the